



SPACE COMPONENTS NEWSLETTER November 2022

New entries to ESCC EPPL and QPL

The following parts have been listed as new entries to the EPPL issued August 2022 and can be purchased through Protec GmbH.



EV12AQ600 EPPL listed; Flight Heritage

ADC with sampling up to 6.4GSPS



- 1 Vpp 100 Ω differential DC/AC coupled input voltage
- 100 Ω Differential input AC coupled clock
- Cross-point switch enabling 1, 2 or 4 channel mode at 6.4 GSps / 3.2 GSps / 1.6 GSps
- 4.5 / 6.5 GHz selectable analog input bandwidth (-3dB)
- Low Latency ESIstream serial link 12.8 Gbps
- Clock and SYNC chaining



ESCC QPL European Rad Hard by Design FPGA

SRAM Based FPGA



NG-medium

34k LUTs 3Mb RAM 112 DSPs 16x SpW & DDR2 PHY

μP Soft IP Leon3 @ 35MHz

VLORIMEV

EPPL listed LVDS Driver & Receiver



- ANSI EIA/TIA644A std
- 500Mbps Channel data rate
- · Low Channel skew and jitter
- · Cold-spare in all pins
- TID: 300 kRad(Si) / SEL immune
- BER <10-13 err/bit (GEO Orbit)
- Improved compatibility with SpaceWire standard